

Amendments to the Claims:

Without prejudice, this listing of the claims replaces all prior versions and listings of the claims in the present application:

Listing of Claims:

1. (Currently Amended) A method for protecting a microcomputer system from manipulation of data stored in a storage arrangement of the microcomputer system, the microcomputer system including a microcomputer allocated to the storage arrangement, comprising the steps of:

causing the microcomputer to access the storage arrangement for processing the data; and before the storage arrangement is accessed, performing the steps of:

assigning an individual identifier to one of the allocated microcomputer and the storage arrangement,

generating a comparison code and storing the comparison code in the storage arrangement as a function of the individual identifier, and

at a time that is one of before and during an operation of the microcomputer system, generating a security code as a function of the individual identifier and comparing the security code with the comparison code, wherein the microcomputer accesses the storage arrangement only if the security code agrees with the comparison code.

2. (Original) The method according to claim 1, wherein:

the data corresponds to a program.

3. (Original) The method according to claim 1, wherein:

a program stored in the storage arrangement is protected.

4. (Original) The method according to claim 1, wherein before the storage arrangement is accessed, the method further comprises the steps of:

storing the individual identifier as the comparison code in the storage arrangement; and

at the time that is one of before and during the operation of the microcomputer system, performing a check as to whether the comparison code agrees with the individual identifier, used as the security code, of the allocated microcomputer.

5. (Original) The method according to claim 1, wherein:

the storage arrangement normally cooperates with the allocated microcomputer only when the security code agrees with the comparison code.

6. (Original) The method according to claim 1, wherein:

before an operation of the storage arrangement, after every start-up of the storage arrangement, the security code is generated and is compared with the comparison code.

7. (Original) The method according to claim 6, further comprising the step of:

placing the storage arrangement in a mode in which, after every start-up, the storage arrangement is switched from an inactive state to an active state only when the security code agrees with the comparison code.

8. (Original) The method according to claim 6, further comprising the step of:

placing the allocated microcomputer in a mode in which, after every start-up, the allocated microcomputer is switched from an inactive to an active state only when the security code agrees with the comparison code.

9. (Original) The method according to claim 1, further comprising the steps of:

executing a validation program stored in a read-only memory of the allocated microcomputer;

determining a code word in the validation program from at least one part of a memory content of the storage arrangement in accordance with a key; and

comparing the code word with a comparison code word stored in the storage arrangement.

10. (Currently Amended) A storage arrangement in which data are stored and to which is allocated at least one microcomputer that accesses the storage arrangement for processing the data, comprising:

an arrangement for storing a comparison code that is generated as a function of an individual identifier assigned to one of the at least one microcomputer and the storage arrangement; and

an arrangement for, at a time that is one of before and during an operation of the storage arrangement, generating a security code as a function of the individual identifier and for comparing the security code with the comparison code, wherein the microcomputer accesses the storage arrangement only if the security code agrees with the comparison code.

11. (Original) The storage arrangement according to claim 10, wherein:

the data correspond to a program.

12. (Original) The storage arrangement according to claim 10, wherein:

the storage arrangement is capable of being placed in a mode in which, after every start-up, the storage arrangement is switched from an inactive state to an active state only when the security code agrees with the comparison code.

13. (Original) The storage arrangement according to claim 10, wherein:
the storage arrangement corresponds to a flash memory.

14. (Currently Amended) A microcomputer system, comprising:
a microcomputer; and
a storage arrangement assigned to the microcomputer, wherein:
data are stored in the storage arrangement,
the microcomputer accesses the storage arrangement in order to process the data,

in the storage arrangement, a comparison code that is generated as a function of an individual identifier assigned to one of the microcomputer and to the storage arrangement is stored, and

the microcomputer includes an arrangement for, at a time that is one of before and during an operation of the microcomputer system, for generating a security code as a function of the individual identifier and to compare the security code with the comparison code, wherein the microcomputer accesses the storage arrangement only if the security code agrees with the comparison code.

15. (Original) The microcomputer according to claim 14, wherein:
the data correspond to a program.

16. (Original) The microcomputer according to claim 14, wherein:

the microcomputer is capable of being placed in a mode in which, after every start-up, the microcomputer is switched from an inactive state to an active state only when the security code agrees with the comparison code.